

**DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING**  
**PONDICHERRY ENGINEERING COLLEGE**  
**SCHEDULE FOR AUTONOMOUS B.Tech., M.Tech. and M.C.A. PRACTICAL EXAMINATIONS – DEC 2018**

Class	Name of the Lab	To be Held At	Date	No of Batches	Register Number
I Sem B.Tech	CS202: Programming Lab	Computer Centre Lab	17/12/18	3	18EC1001 to 18EC1045 and 18EC1063 (45) + Arrears (CS102 Old Regulation)
			18/12/18	3	18EC1046 to 18EC1089 (43) + Arrears (CS102 Old Regulation)
			19/12/18	3	18EE1001 to 18EE1060 (59)
			20/12/18	3	18CE1001 to 18CE1066, 18CH1012 and 18IT1004 (67)
			21/12/18	3	18EI1001 to 18EI1060 (59)
I Sem M.C.A	CA156: Operating System Lab	Applications Lab	20/12/18	1	Roll No. 1 to 12 + Arrears
	CA155: C and Data Structures Lab	Distributed Computing Lab	19/12/18	1	Roll No. 1 to 12 + Arrears
III Sem M.C.A	CA167 :Database Management Systems Lab	Applications Lab	18/12/18	1	Roll No. 1 to 35 + Arrears
	CA 168 :Computer Networks Laboratory	Hardware and Troubleshooting Lab	17/12/18	1	Roll No. 1 to 35 + Arrears
I SEM M.Tech (DCS)	CS155: Advanced Software Laboratory -I	Distributed Computing Lab	21/12/18	1	Roll No. 1 to 9
I SEM M.Tech (IS)	CS166:Information Security Laboratory- I	Information Security Lab	21/12/18	1	Roll No. 1 to 23
IV SEM M.Tech (IS)	CS171: Project Phase II	-	21/12/18	-	1
VI Sem M.C.A	CA179: Project Work	-	21/12/18	-	1

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**HEAD OF THE DEPARTMENT**

1718  
28-11-18

**Pondicherry Engineering College, Puducherry**

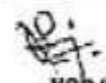
(An Autonomous Institution of Government of Puducherry)

**TIME TABLE-End Semester Practical Examination - December 2018 (B.Tech. First Year -CE, EC, EE and EI)**

Sl.No.	Date	Name of the Lab	Semester	Session	Reg. No	No of students
1.	17.12.2018	EE202 - Electrical Engineering Laboratory	EC2	8.30 am-11.30 am	18EC1046 - 18EC1060	15
				11.30 am-2.30 pm	18EC1061- 18EC1076	15
				2.30 pm-5.30 pm	18EC1077 - 18EC1089	13
2.	18.12.2018	EE202 - Electrical Engineering Laboratory	EC1	8.30 am-11.30 am	18EC1001 - 18EC1015	15
				11.30 am-2.30 pm	18EC1016-18EC1030	15
				2.30 pm-5.30 pm	18EC1031-18EC1045 and 18EC1063	15
3.	19.12.2018	EE202 - Electrical Engineering Laboratory	EIE	8.30 am-11.30 am	18EI1001 - 18EI1020	20
				11.30 am-2.30 pm	18EI1021 - 18EI1040	20
				2.30 pm-5.30 pm	18EI1041 - 18EI1060	19
4.	20.12.2018	EE202 - Electrical Engineering Laboratory	EE1	8.30 am-11.30 am	18EE1001-18EE1021	20
				11.30 am-2.30 pm	18EE1022 - 18EE1041	20
				2.30 pm-5.30 pm	18EE1042 - 18EE1060	15
5.	21.12.2018	EE202 - Electrical Engineering Laboratory	CE	8.30 am-11.30 am	18CE1001 - 18CE1024	23
				11.30 am-2.30 pm	18CE1025 - 18CE1046	22
				2.30 pm-5.30 pm	18CE1047 - 18CE1066 18CH1012 and 18IT1004	22
6.	21.12.2018	M.Tech (EDC) EE154 - Solid State Systems Laboratory EE157 Electrical Drives Laboratory	I/II	9.30 am - 12.30 pm and Arrear lab	18EE301 - 18EE310 + Arrears	10 + Arrears

**Note:** 1. Batches are allotted according to the branch wise Register number uploaded in the IIS web portal.

2. Arrear Students are advised to meet the HOD of the respective department and register their names in advance for appearing for the lab examination without overlapping with the theory and other practical exams.

  
**HOD/EEE**  
 Head of EEE Department  
 Pondicherry Engineering College  
 Puducherry - 605 015